

The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Previously Presented) A method of driving a display device comprising the steps of:

providing an original video signal;

modifying the original video signal to a pair of video signals having a reversal relation to each other;

inputting the pair of video signals to one source driver circuit;

applying one of the pair of video signals to an odd signal line of the signal lines of a pixel region in accordance with a signal from a first shift register included in the source driver circuit; and

applying the other of the pair of video signals to an even signal line of the signal lines of the pixel region in accordance with a signal from a second shift register included in the source driver circuit.

2. (Previously Presented) A method of driving a display device comprising the steps of:

providing an original video signal;

modifying the original video signal to a pair of video signals having symmetry with reference to a potential of an opposite electrode provided opposite to pixel electrodes;

inputting the pair of video signals to one source driver circuit;

applying one of the pair of video signals to an odd signal line of the signal lines of a pixel region in accordance with a signal from a first shift register included in the source driver circuit; and

applying the other of the pair of video signals to an even signal line of the signal lines of the pixel region in accordance with a signal from a second shift register included in the source driver circuit.

3. (Previously Presented) A method of driving a display device comprising the steps of:

- providing an original video signal;
- modifying the original video signal to plural pairs of video signals having reversal relation to each other;
- inputting the pair of video signals to one source driver circuit;
- applying one of the pair of video signals to an odd signal line of the signal lines of a pixel region in accordance with a signal from a first shift register included in the source driver circuit; and
- applying the other of the pair of video signals to an even signal line of the signal lines of the pixel region in accordance with a signal from a second shift register included in the source driver circuit.

4. (Previously Presented) A method of driving a display device comprising the steps of:

- providing an original video signal;
- modifying the original video signal to plural pairs of video signals, each pair having symmetry with reference to a potential of an opposite electrode provided opposite to a pixel electrode;
- inputting the pair of video signals to one source driver circuit;
- applying one of the pair of video signals to an odd signal line of the signal lines of a pixel region in accordance with a signal from a first shift register included in the source driver circuit; and

applying the other of the pair of video signals to an even signal line of the signal lines of the pixel region in accordance with a signal from a second shift register included in the source driver circuit.

5. (Previously Presented) A method of driving a display device comprising the steps of:

providing an original video signal;

modifying the original video signal to at least one first video signal and at least one second video signal;

applying the first video signal to a source driver circuit through a first single video signal line;

applying the second video signal to the source driver circuit through a second single video signal line;

inverting polarities of signal potentials of the first video signal and the second video signal in every frame period,

applying the first video signal to an odd signal line of the signal lines of a pixel region in accordance with a signal from a first shift register included in the source driver circuit; and

applying the second video signal to an even signal line of the signal lines of the pixel region in accordance with a signal from a second shift register included in the source driver circuit,

wherein the first video signal has a reversal relationship with the second video signal.

6. (Original) A method of driving a display device according to claim 5, wherein polarities of adjacent pixel electrodes in a lateral direction are opposite to each other with respect to an opposite electrode and the polarities of every pixel electrode is inverted every frame period.

7. (Original) The method according to claim 5 wherein said display device is driven in a source line inversion method.

8. (Previously Presented) A method of driving a display comprising the steps of:  
providing an original video signal;  
modifying the original video signal to at least one first video signal and at least one second video signal;  
applying the first video signal to a source driver circuit through a first single video signal line;  
applying the second video signal to the source driver circuit through a second single video signal line;  
inverting polarities of signal potentials of the first video signal and the second video signal in every horizontal period,  
applying the first video signal to an odd signal line of the signal lines of a pixel region in accordance with a signal from a first shift register included in the source driver circuit; and  
applying the second video signal to an even signal line of the signal lines of the pixel region in accordance with a signal from a second shift register included in the source driver circuit,  
wherein the first video signal has a reversal relationship with the second video signal.

9. (Original) A method of driving a display device according to claim 8, wherein polarities of adjacent pixel electrodes in both lateral and vertical directions are opposite to each other with respect to an opposite electrode and said polarities are inverted every frame period.

10. (Original) The method according to claim 8 wherein said display device is driven in a dot inversion method.

11. (Previously Presented) A display device comprising:  
a liquid crystal panel including a switching element for each of pixel electrodes;  
a scanning line driver circuit for driving scanning lines of the liquid crystal panel;  
a signal line driver circuit for driving signal lines of the liquid crystal panel;  
a signal processing circuit; and  
a control circuit for controlling drive of the liquid crystal panel and the signal processing circuit,

wherein the signal processing circuit is connected to the liquid crystal panel through a plurality of video signal lines, and includes D/A conversion circuits connected to the plurality of video signal lines, the number of D/A conversion circuits being equal to the number of video signal lines.

12. (Original) A display device according to claim 11, wherein the picture display device is a projection type display means including a transmission type liquid crystal panel and a light source for projection.

13. (Previously Presented) The method according to claim 1 wherein a first start pulse signal and a first clock signal are inputted to the first shift register, and wherein a second start pulse signal and a second clock signal are inputted to the second shift register.

14. (Currently Amended) The method according to claim 2 wherein a first start pulse signal and a first clock signal are inputted to the first shift register, and wherein a second start pulse signal and a second clock signal are inputted to the second shift register.

15. (Currently Amended) The method according to claim 3 wherein a first start pulse signal and a first clock signal are inputted to the first shift register, and wherein a second start pulse signal and a second clock signal are inputted to the second shift register.

16. (Currently Amended) The method according to claim 4 wherein a first start pulse signal and a first clock signal are inputted to the first shift register, and wherein a second start pulse signal and a second clock signal are inputted to the second shift register.

17. (Currently Amended) The method according to claim 5 wherein a first start pulse signal and a first clock signal are inputted to the first shift register, and wherein a second start pulse signal and a second clock signal are inputted to the second shift register.

18. (Currently Amended) The method according to claim 8 wherein a first start pulse signal and a first clock signal are inputted to the first shift register, and wherein a second start pulse signal and a second clock signal are inputted to the second shift register.

19. (Previously Presented) The display device according to claim 11 wherein said signal line driver circuit comprises two shift registers.